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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	10/519,700
				Filing Date	09/14/2005
				First Named Inventor	Wade A. Krull
				Group Art Unit	1765
				Examiner Name	Angadi, Maki A
Sheet	1	of	1	Attorney Docket Number	211843-00030

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number – Kind Code ² (if known)			
	A1	5,930,634	07-27-1999	Hause Frederick N et al.	
	A2	6,297,109	10-02-2001	Chan Lap et al.	
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		Country Code ³ – Number ⁴ – Kind Code ⁵ (if known)				
	C1	WO 01/43175A	06-14-2001	Infineon Technologies		
	C2	WO 99/65070A	12-16-1999	Philips Svenska		
	C3	EP 0 219 243 A	04-22-1987	Monolithic Memories		
	C4	EP 1 093 149 A	04-18-2001	Axcelis Tech Inc		
	C5	WO 01/69668	09-20-2001	Advanced Micro Devices Inc.		

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher city and/or country where published	T ²
		Schmitz J. ET AL: "Ultra-shallow junction formation by outdiffusion from implanted oxide" 195 th Meeting of the Electrochemical Society, 1998, pages 1000-1012, XP002434976 Washington	
		FAIR R: "Physical models of boron diffusion in ultrathin gate oxides" Journal of the Electrochemical Society, vol 144, no. 2, February 1997 (1997-2002), pages 708-717, XP002434977	
Examiner Signature			Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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